

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	15239	high adj3 (processor or microprocessor or cpu or mpu)	USPA T; US-P GPUB	2004/08/19 15:36	
2	BRS	L2	7181	low adj3 (processor or microprocessor or cpu or mpu)	USPA T; US-P GPUB	2004/08/19 15:37	
3	BRS	L3	2397	1 and 2	USPA T; US-P GPUB	2004/08/19 15:37	
4	BRS	L4	115	pentium same arm	USPA T; US-P GPUB	2004/08/19 15:48	
5	BRS	L5	1	3 and 4	USPA T; US-P GPUB	2004/08/19 15:49	
6	BRS	L6	16717	dual near3 purpose	USPA T; US-P GPUB	2004/08/19 15:50	
7	BRS	L7	1	4 and 6	USPA T; US-P GPUB	2004/08/19 15:52	
8	BRS	L8	67	3 and 6	USPA T; US-P GPUB	2004/08/19 15:57	
9	BRS	L9	1465	personal adj2 organizer	USPA T; US-P GPUB	2004/08/19 15:58	
10	BRS	L10	22	2 and 9	USPA T; US-P GPUB	2004/08/19 16:13	
11	BRS	L11	40417	(portable or laptop) adj2 computer	USPA T; US-P GPUB	2004/08/19 16:14	
12	BRS	L12	446	9 and 11	USPA T; US-P GPUB	2004/08/19 16:14	
13	BRS	L13	9	3 and 12	USPA T; US-P GPUB	2004/08/19 16:22	

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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	123268	integrated near3 (device or apparatus or computer)	USPA T; US-P GPUB	2004/08/1 9 16:23	
15	BRS	L15	98	12 and 14	USPA T; US-P GPUB	2004/08/1 9 16:23	
16	BRS	L16	3	6 and 15	USPA T; US-P GPUB	2004/08/1 9 16:26	
17	BRS	L17	1886	713/300.ccls. 713/320.ccls.	USPA T; US-P GPUB	2004/08/1 9 16:27	
18	BRS	L18	1309	713/322-324.ccls.	USPA T; US-P GPUB	2004/08/1 9 16:30	
19	BRS	L19	2793	17 or 18	USPA T; US-P GPUB	2004/08/1 9 16:31	
20	BRS	L20	95	15 not 16	USPA T; US-P GPUB	2004/08/1 9 16:31	
21	BRS	L21	3	19 and 20	USPA T; US-P GPUB	2004/08/1 9 16:31	

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